

# Computations on Ordinals

BY PETER KOEPKE

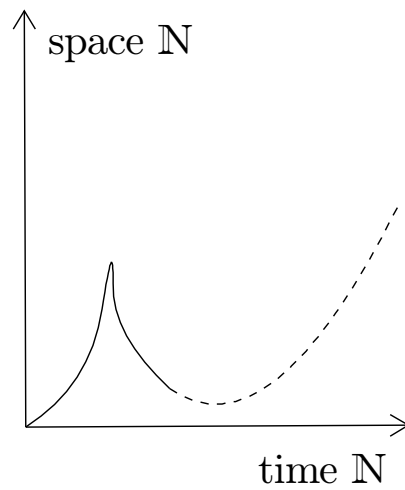
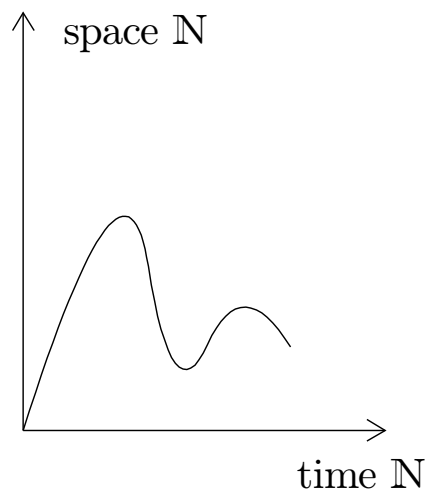
University of Bonn

*IMSC, Chennai, September 16, 2008*

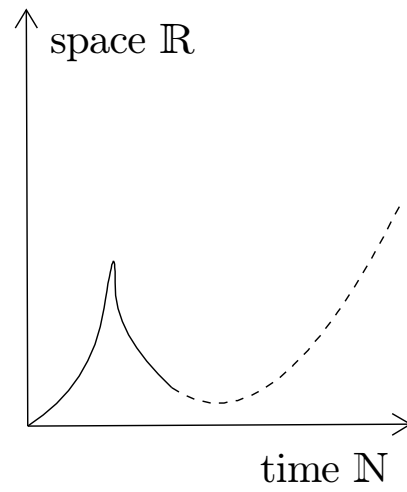
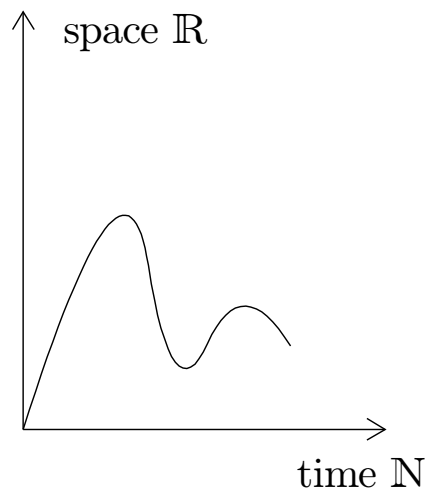
# A standard TURING computation

	⋮	⋮	⋮	⋮	⋮		⋮	⋮		
	$n+1$	0	0	0	0	0	0	1	...	...
↑	$n$	0	0	0	0	0	...	1	1	
	⋮	0	0	0	0	0	...	0	0	
S	4	0	0	0	0	0	...	0	0	
P	3	0	0	0	0	0	...	0	0	
A	2	0	0	1	1	1	...	1	1	
C	1	0	1	1	0	0	...	0	0	
E	0	1	1	1	1	0	...	1	1	
		0	1	2	3	4	...	$n$	$n+1$	...
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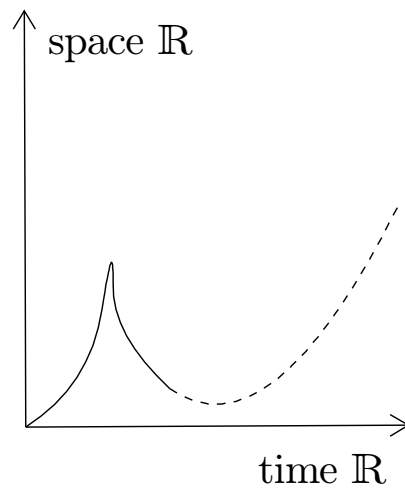
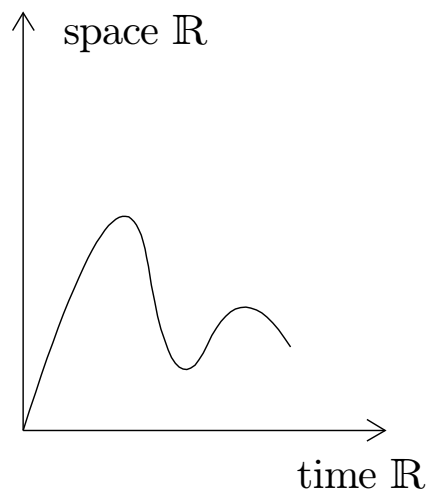
# The shape of standard Turing computations



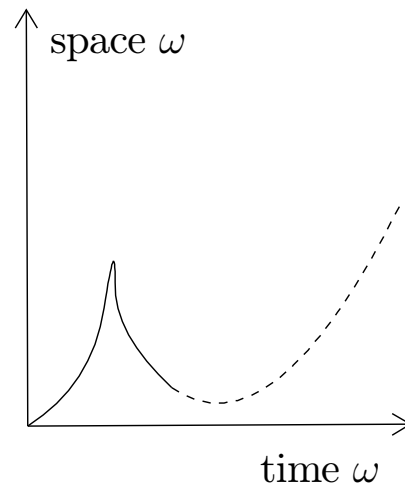
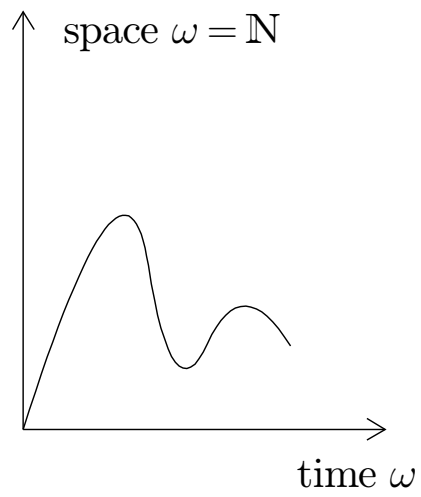
# The shape of BSS computations



# Real functions, differential equations, dynamical systems



Standard Turing computations are based on the *ordinal*  $\omega = \mathbb{N}$



## Ordinals

Natural numbers:

$$0 = \emptyset, 1 = \{0\}, 2 = \{0, 1\}, \dots, n = \{0, 1, \dots, n-1\}, \dots$$

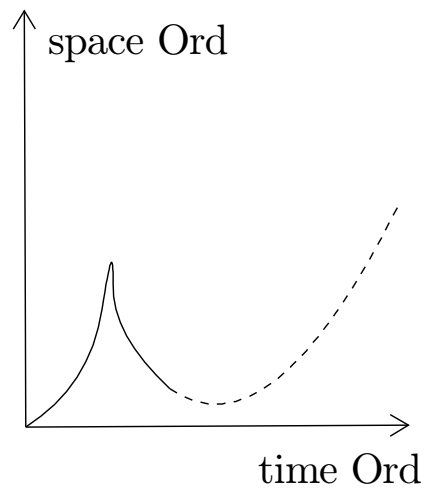
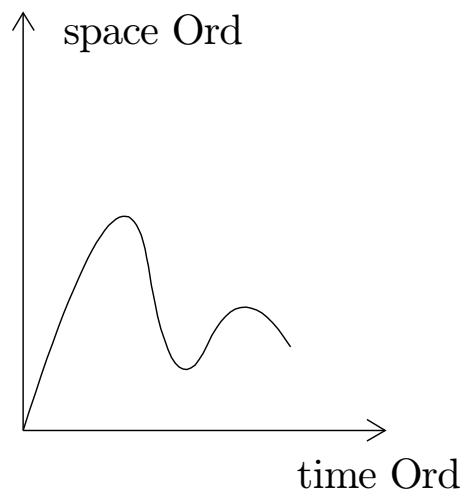
$$\omega = \mathbb{N} = \{0, 1, 2, \dots, n, \dots\}$$

Ordinal numbers:

$$0, 1, 2, \dots, n, \dots, \omega, \omega + 1 = \omega \cup \{\omega\}, \dots, \alpha, \alpha + 1 = \alpha \cup \{\alpha\}, \dots, \aleph_1, \dots, \aleph_\omega, \dots$$

$$\infty = \text{Ord} = \{0, 1, 2, \dots, \omega, \dots, \alpha, \dots\}$$

# Ordinal computations





## Limit ordinals and ordinal limits

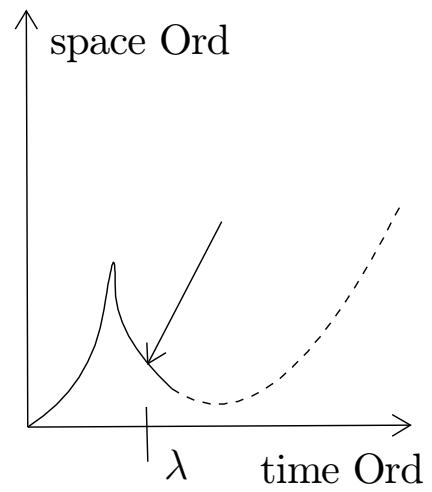
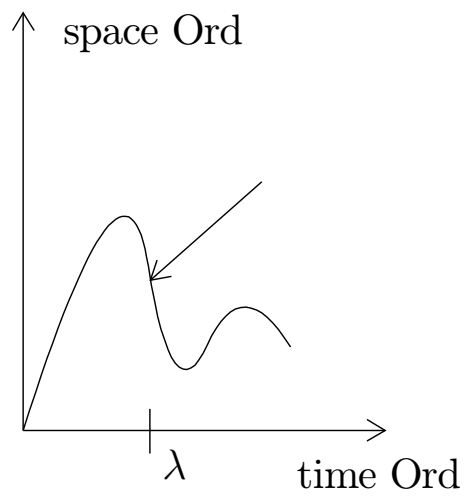
An ordinal  $\lambda$  is a *limit ordinal*, if it is not of the form  $\lambda = 0$  or  $\lambda = \mu + 1$ .

Let  $\{\alpha_\xi \mid \xi < \lambda\} \subseteq \text{Ord}$ .

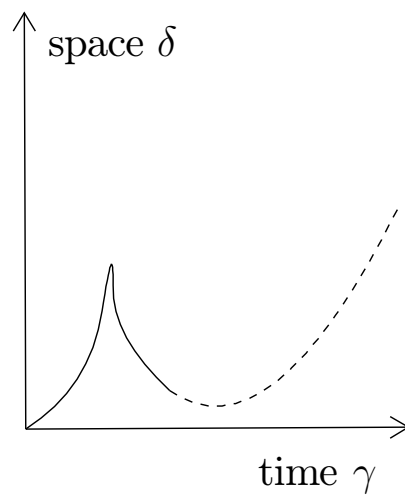
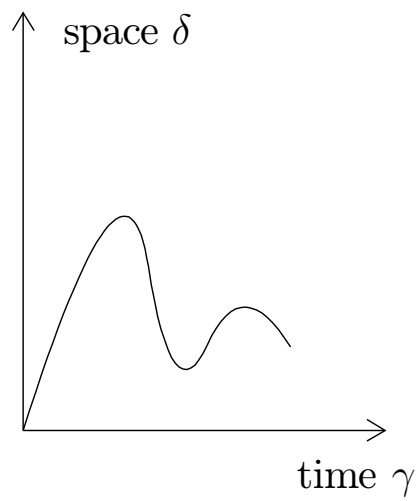
$\sup_{\xi < \lambda} \alpha_\xi = \bigcup_{\xi < \lambda} \alpha_\xi \in \text{Ord}$ ,  $\min_{\xi < \lambda} \alpha_\xi = \bigcap_{\xi < \lambda} \alpha_\xi \in \text{Ord}$ .

$\liminf_{\xi < \lambda} \alpha_\xi = \sup_{\zeta < \lambda} (\min_{\zeta \leq \xi < \lambda} \alpha_\xi)$ .

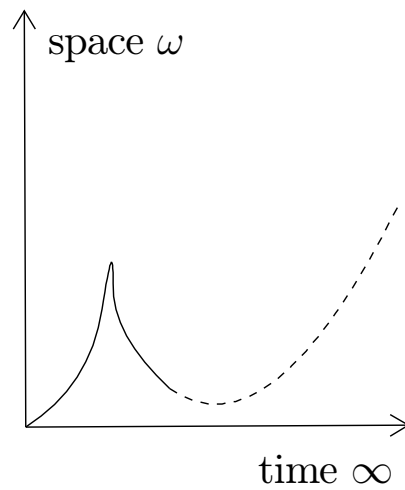
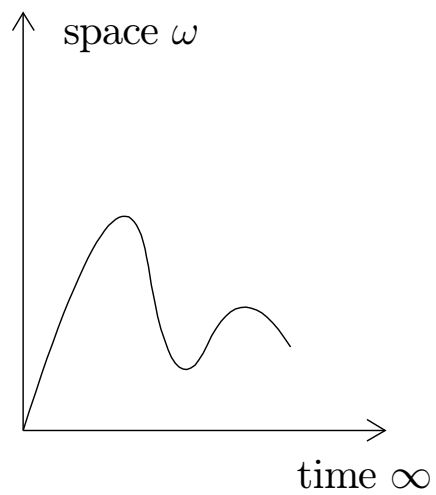
# Ordinal computations: $\liminf$ at limit ordinals



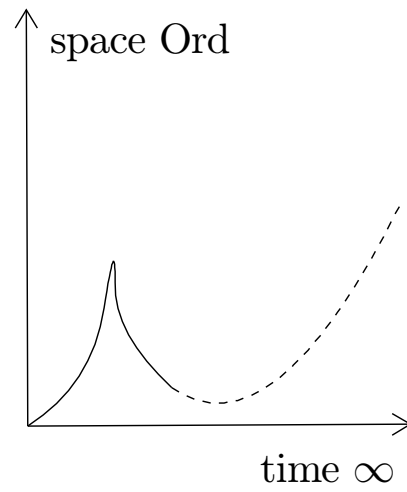
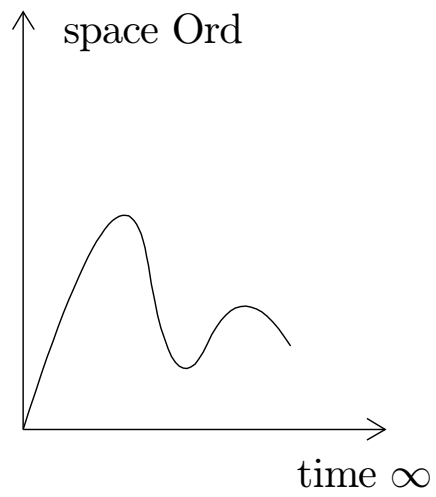
# $\gamma$ - $\delta$ -computations



ITTM computations are  $\infty$ - $\omega$ -computations



# Ordinal register machines (ORM) (with Ryan Siders)



A *register program* is a finite list  $P = I_0, I_1, \dots, I_{s-1}$  of *instructions*:

- a) the *zero instruction*  $Z(n)$  set register  $R_n$  to 0;
- b) the *successor instruction*  $S(n)$  increases register  $R_n$  by 1;
- c) the *oracle instruction*  $O(n)$  sets register  $R_n$  to 1 if its content is an element of the oracle, and to 0 otherwise;
- d) the *transfer instruction*  $T(m, n)$  sets  $R_n$  to the contents of  $R_m$ ;
- e) the *jump instruction*  $J(m, n, q)$ : if  $R_m = R_n$ , the register machine proceeds to the  $q$ th instruction of  $P$ ; otherwise it proceeds to the next instruction in  $P$ .

Let  $P = P_0, P_1, \dots, P_{k-1}$  be a register program. A pair

$$S: \theta \rightarrow \omega, R: \theta \rightarrow ({}^\omega \text{Ord})$$

is the ORM *computation* by  $P$  with oracle  $Z \subseteq \text{Ord}$  if:

- a)  $\theta$  is a successor ordinal or  $\theta = \text{Ord}$ ;  $\theta$  is the *length* of the computation;
- b)  $S(0) = 0$ ; the machine starts in state 0;
- c) If  $t < \theta$  and  $S(t) \notin s = \{0, 1, \dots, s-1\}$  then  $\theta = t+1$ ; the machine *stops* if the machine state is not a program state of  $P$ ;
- d) If  $t < \theta$  and  $S(t) \in \{0, 1, \dots, s-1\}$  then  $t+1 < \theta$ ; the next configuration is determined by the instruction  $P_{S(t)}$ : .....

e) If  $t < \theta$  is a limit ordinal, the machine constellation at  $t$  is determined by taking inferior limits:

$$\forall k \in \omega \ R_k(t) = \liminf_{r \rightarrow t} R_k(r);$$

$$S(t) = \liminf_{r \rightarrow t} S(r).$$

...

→ 17:begin loop

...

21: begin subloop

...

29: end subloop

...

32:end loop

...



$\infty$ - $\infty$ -computability, **Ordinal Register Machines** (with Ryan Siders)

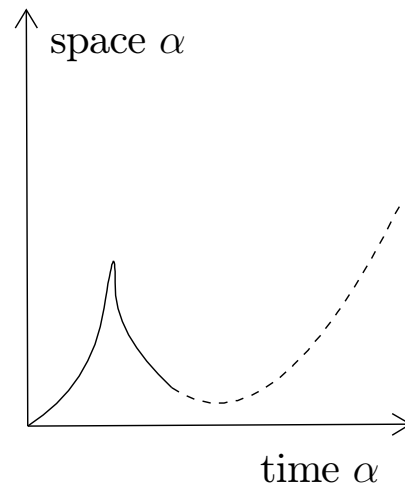
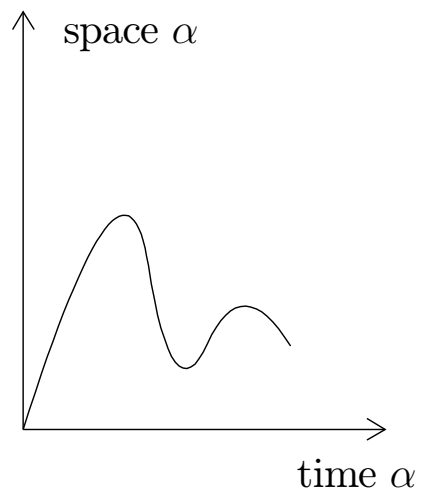
$x \subseteq \text{Ord}$  is ORM *computable* (from parameters) if there are a program  $P$  and ordinals  $\delta_1, \dots, \delta_{n-1}$  such that

$$\forall \alpha \ P: (\alpha, \delta_1, \dots, \delta_{n-1}, 0, 0, \dots) \mapsto \chi_x(\alpha),$$

where  $\chi_x$  is the characteristic function of  $x$ .

**Theorem.**  $x \subseteq \text{Ord}$  is ORM *computable* iff  $x \in L$ , where  $L$  is GÖDEL's inner model of constructible sets.

$\alpha$ - $\alpha$ -computations for admissible  $\alpha$  (with Benjamin Seyfferth)



**Theorem.** Let  $\alpha$  be an admissible ordinal and  $X \subseteq \alpha$ . Then

- a)  $X$  is computable by an  $\alpha$ - $\alpha$ -register machine in parameters  $< \alpha$  iff  $X \in \mathbf{\Delta}_1(L_\alpha)$
- b)  $X$  is computably enumerable by an  $\alpha$ - $\alpha$ -register machine in parameters  $< \alpha$  iff  $X \in \mathbf{\Sigma}_1(L_\alpha)$

One can characterize when a limit ordinal  $\beta$  is admissible using  $\beta$ - $\beta$ -machines.

One can do parts of  $\alpha$  recursion theory using  $\alpha$ - $\alpha$ -machines, e.g., the SACKS-SIMPSON theorem.

## Ordinal register computability

Register machines	space $\omega$	space admissible $\alpha$	space Ord
time $\omega$	standard register machine computable = $\Delta_1^0$	-	-
time admissible $\alpha$	?	$\alpha$ register machine ( $\alpha$ recursion theory) computable = $\Delta_1(L_\alpha)$	-
time Ord	?	?	Ordinal register machine computable = $L \cap \mathcal{P}(\text{Ord})$

## Ordinal TURING computability

TURING	space $\omega$	space admissible $\alpha$	space Ord
time $\omega$	standard TURING machine computable = $\Delta_1^0$	-	-
time admissible $\alpha$	?	$\alpha$ TURING machine ( $\alpha$ -recursion theory) computable = $\Delta_1(L_\alpha)$	-
time Ord	ITTM $\Delta_1^1 \not\subseteq$ computable in real parameter $\subseteq \Delta_2^1$	?	Ordinal TURING machine computable = $L \cap \mathcal{P}(\text{Ord})$

## Ordinal register computability

Register machines	space $\omega$	space admissible $\alpha$	space Ord
time $\omega$	standard register machine computable = $\Delta_1^0$	-	-
time admissible $\alpha$	?	$\alpha$ register machine ( $\alpha$ recursion theory) computable = $\Delta_1(L_\alpha)$	-
time Ord	<b>ITRM</b> Infinite time register machine computable in real parameters = ?	?	Ordinal register machine computable = $L \cap \mathcal{P}(\text{Ord})$

## Infinite Time Register Machines (ITRM) (with Russell Miller)

Let  $P = P_0, P_1, \dots, P_{k-1}$  be a register program. A pair

$$S: \theta \rightarrow \omega, R: \theta \rightarrow ({}^\omega\omega)$$

is the *infinite time register computation* by  $P$  with oracle  $Z \subseteq \omega$  if:

a) ...

b) If  $t < \theta$  is a limit ordinal, the machine constellation at  $t$  is determined by taking inferior limits **or in case of overflow resetting to 0**:

$$\forall k \in \omega R_k(t) = \begin{cases} 0, & \text{if } \liminf_{r \rightarrow t} R_k(r) = \omega, \\ \liminf_{r \rightarrow t} R_k(r), & \text{else;} \end{cases}$$
$$S(t) = \liminf_{r \rightarrow t} S(r).$$

A subset  $A \subseteq \mathcal{P}(\omega) = \mathbb{R}$  is ITRM-*computable* if there is a register program  $P$  and an oracle  $Y \subseteq \omega$  such that for all  $Z \subseteq \omega$ :

$$Z \in A \text{ iff } P: (0, 0, \dots), Y \times Z \mapsto 1, \text{ and } Z \notin A \text{ iff } P: (0, 0, \dots), Y \times Z \mapsto 0$$

where  $Y \times Z$  is the cartesian product of  $Y$  and  $Z$  with respect to the pairing function

$$(y, z) \mapsto \frac{(y+z)(y+z+1)}{2} + z.$$



## Stacks

Code a stack  $(r_0, \dots, r_{m-1})$  of natural numbers by

$$r = 2^m \cdot 3^{r_0} \cdot 5^{r_1} \dots p_m^{r_{m-1}}$$

**Proposition 1.** *Let  $\alpha < \tau$  where  $\tau$  is a limit ordinal. Assume that in some ITRM-computation using a stack, the stack contains  $r = (r_0, \dots, r_{m-1})$  for cofinally many times below  $\tau$  and that all contents in the time interval  $(\alpha, \tau)$  are endextensions of  $r = (r_0, \dots, r_{m-1})$ . Then at time  $\tau$  the stack contents are*

$$r = (r_0, \dots, r_{m-1}).$$

```

push 1; %% marker to make stack non-empty
push 0; %% try 0 as first element of descending sequence
FLAG=1; %% flag that fresh element is put on stack
Loop: Case1: if FLAG=0 and stack=0 %% inf descending seq found
    then begin; output 'no'; stop; end;
Case2: if FLAG=0 and stack=1 %% inf descending seq not found
    then begin; output 'yes'; stop; end;
Case3: if FLAG=0 and length-stack > 1 %% top element cannot be continued infinitely
    then begin; %% try next
        pop N; push N+1; FLAG:=1; %% flag that fresh element is put on stack
        goto Loop;
    end;
Case4: if FLAG=1 and stack-is-decreasing
    then begin;
        push 0; %% try to continue sequence with 0
        FLAG:=0; FLAG:=1; %% flash the flag
        goto Loop;
    end;
Case5: if FLAG=1 and not stack-is-decreasing
    then begin;
        pop N; push N+1; %% try next
        FLAG:=0; FLAG:=1; %% flash the flag
        goto Loop;
    end;

```

**Lemma 2.** *Let  $I: \theta \rightarrow \omega$ ,  $R: \theta \rightarrow ({}^\omega\omega)$  be the computation by  $P$  with oracle  $Z$  and trivial input  $(0, 0, \dots)$ . Then*

a) *If  $Z$  is wellfounded then the computation stops with output ‘yes’.*

b) *If  $Z$  is illfounded then the computation stops with output ‘no’.*

**Theorem 3.** *The set  $\text{WO} = \{Z \subseteq \omega \mid Z \text{ codes a wellorder}\}$  is computable by an ITRM.*

**Theorem 4.** *Every  $\Pi_1^1$  set  $A \subseteq \mathcal{P}(\omega)$  is ITRM-computable.*

ITTM-s can simulate ITRM-s:

Simulate the number  $i$  in register  $R_m$  as an initial segment of  $i$  1's on the  $m$ -th tape of an ITTM.

If  $\lambda$  is a limit time and  $\liminf_{\tau \rightarrow \lambda} R_m(\tau) = i^* \leq \omega$  then the  $m$ -th tape will hold an initial segment of  $i^*$  1's.

OK, if  $i^*$  is finite.

If  $i^* = \omega$ , this may be detected by a subroutine which then *resets* the  $m$ -th tape to 0.

Since ITTM-decidable  $\subsetneq \Delta_2^1$ :

## Ordinal register computability

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time admissible $\alpha$	?	$\alpha$ register machine ( $\alpha$ recursion theory) computable = $\Delta_1(L_\alpha)$	-
time Ord	ITRM $\Delta_1^1 \not\subseteq$ computable in real parameter $\not\subseteq \Delta_2^1$	?	Ordinal register machine computable = $L \cap \mathcal{P}(\text{Ord})$